

FIG. 1

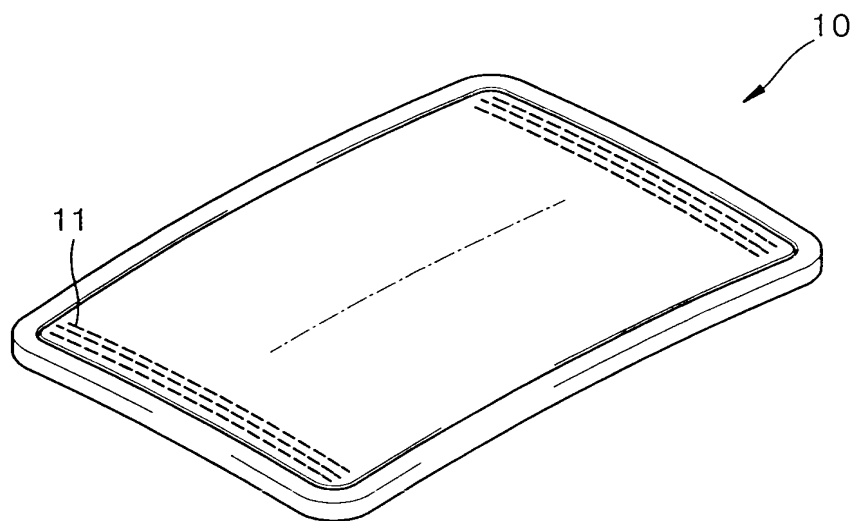


FIG. 2A

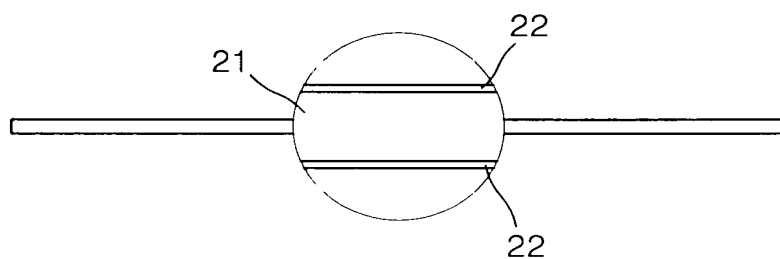


FIG. 2B

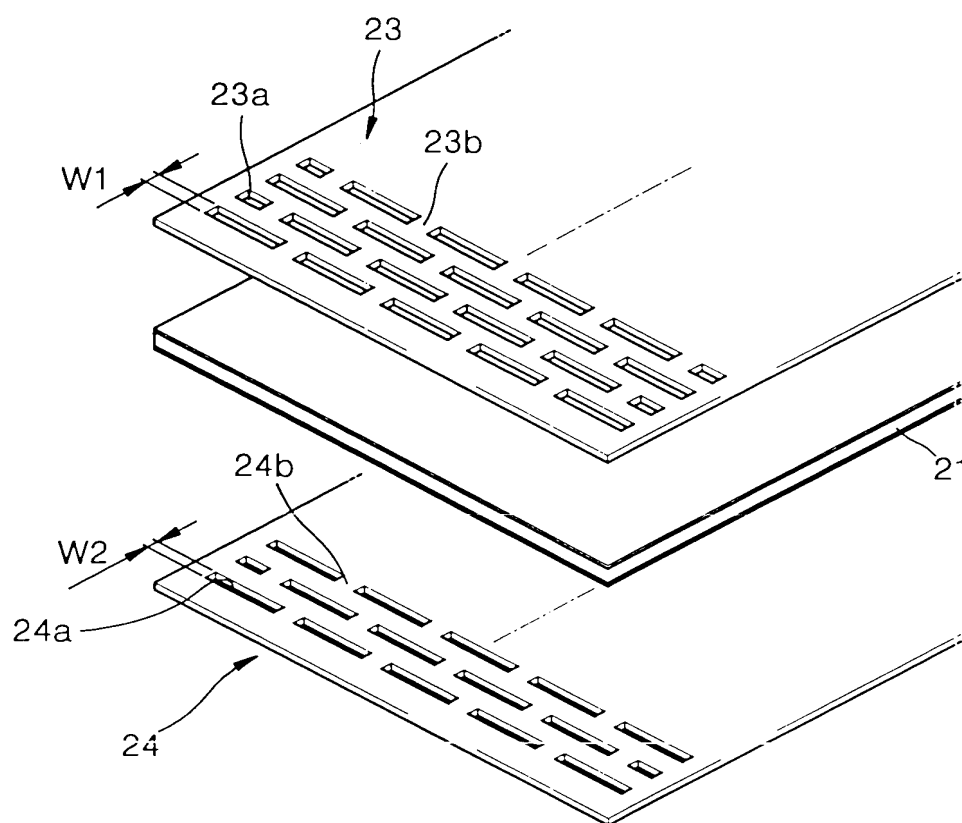


FIG. 2C

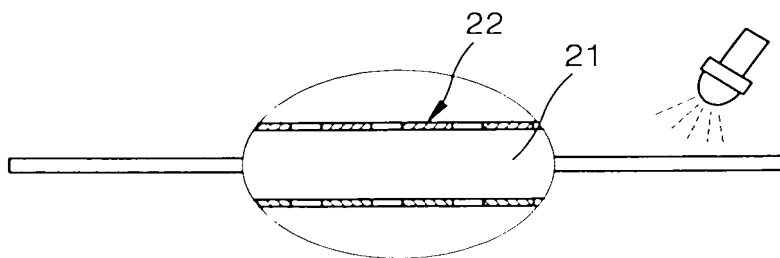


FIG. 3

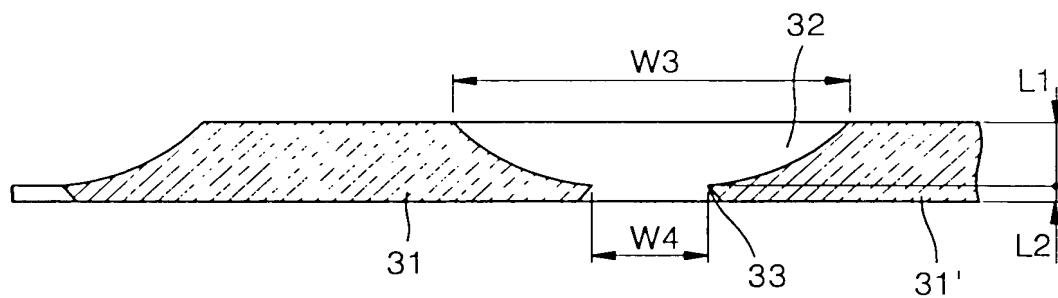


FIG. 5

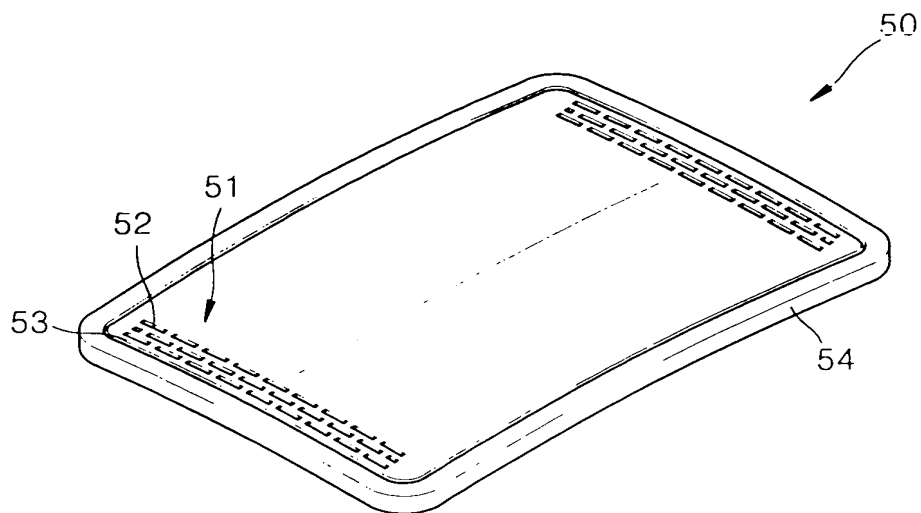


FIG. 4

40

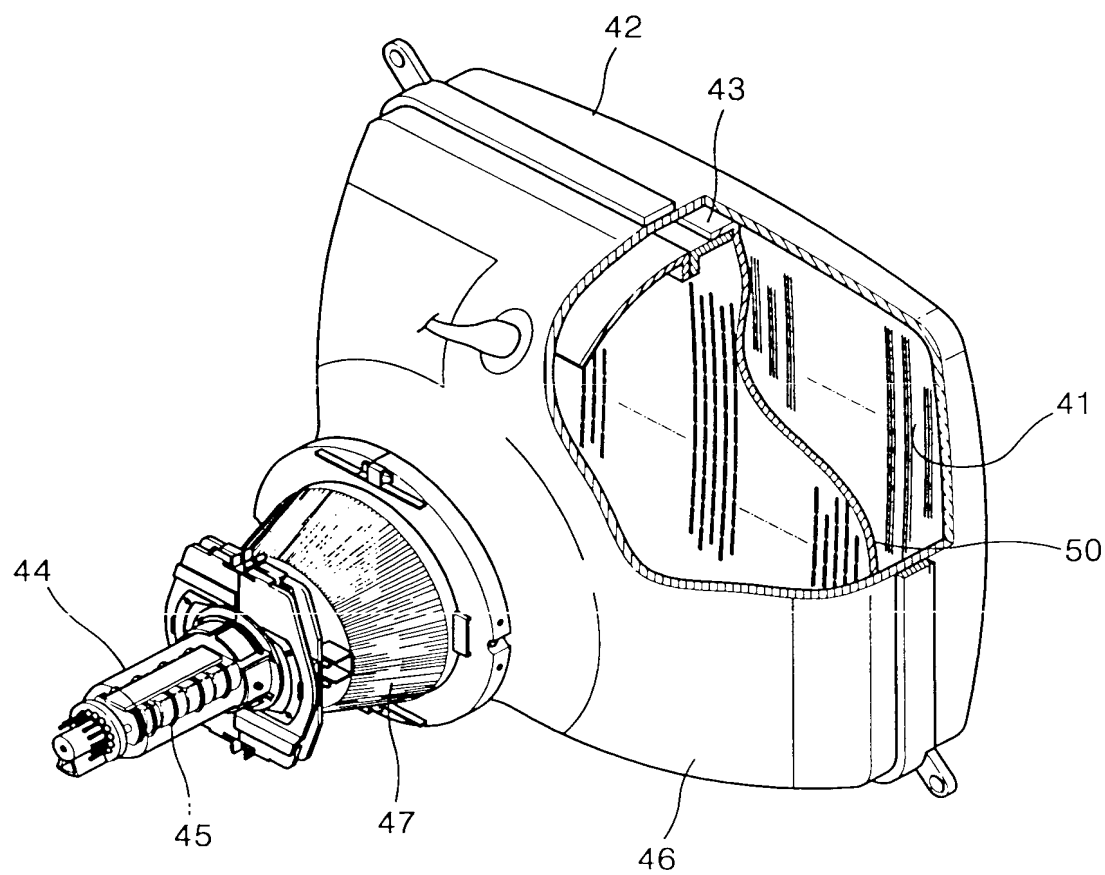


FIG. 6

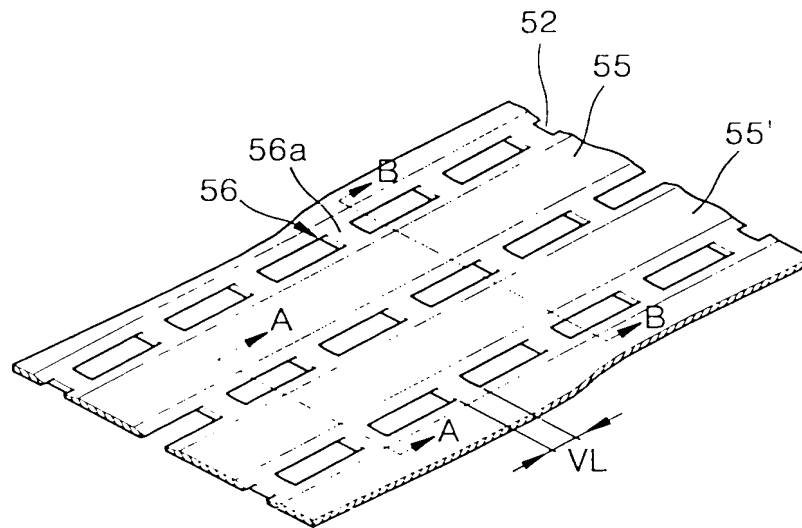
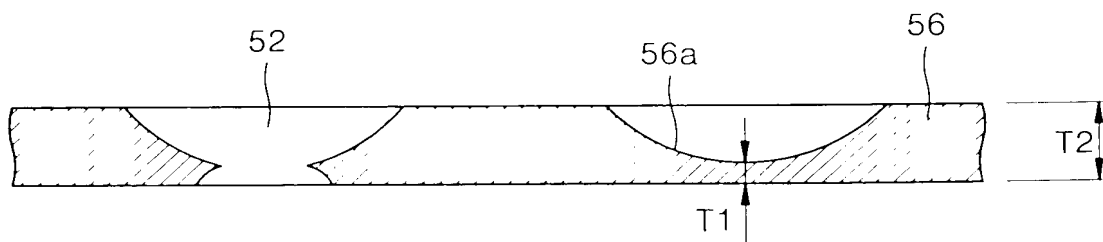
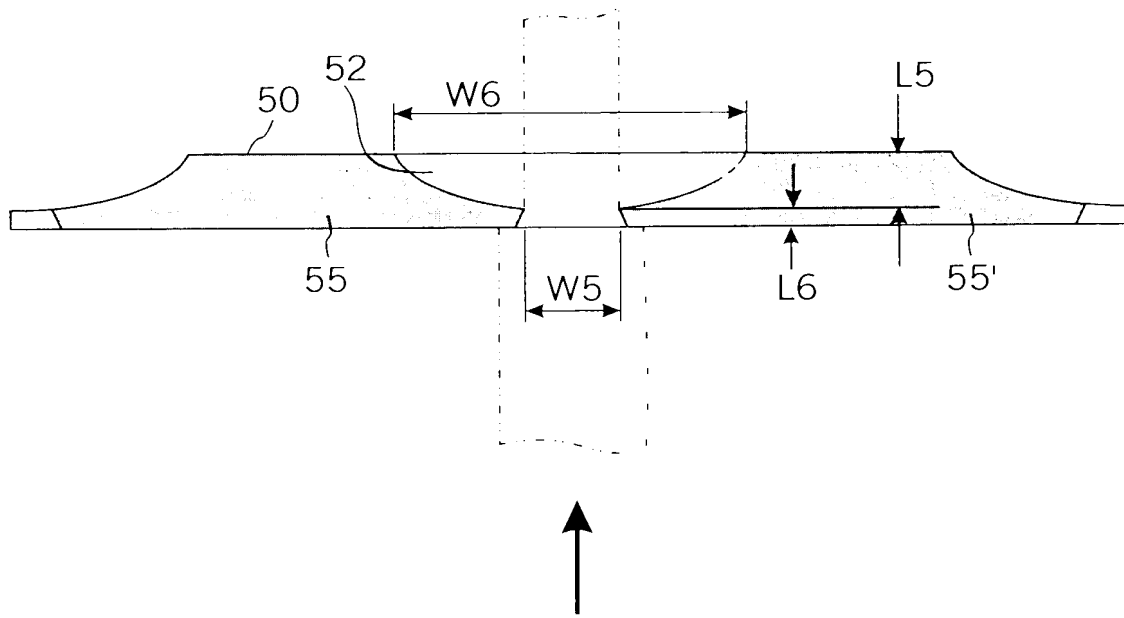
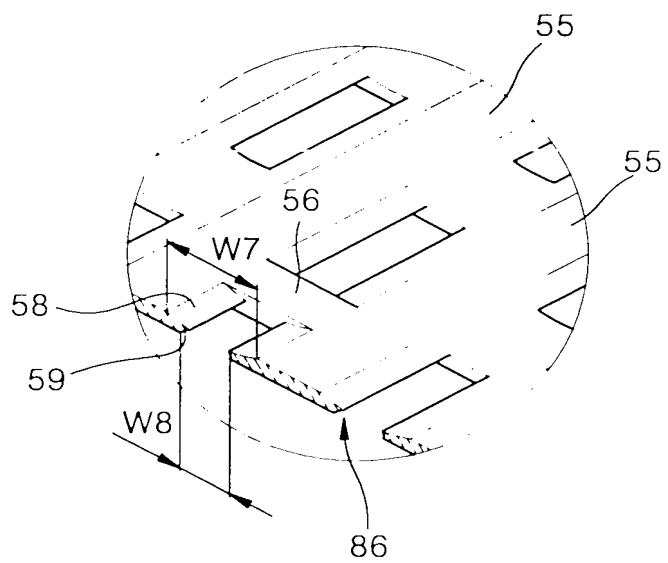


FIG. 7





**FIG. 8**



**FIG. 9**

FIG. 10

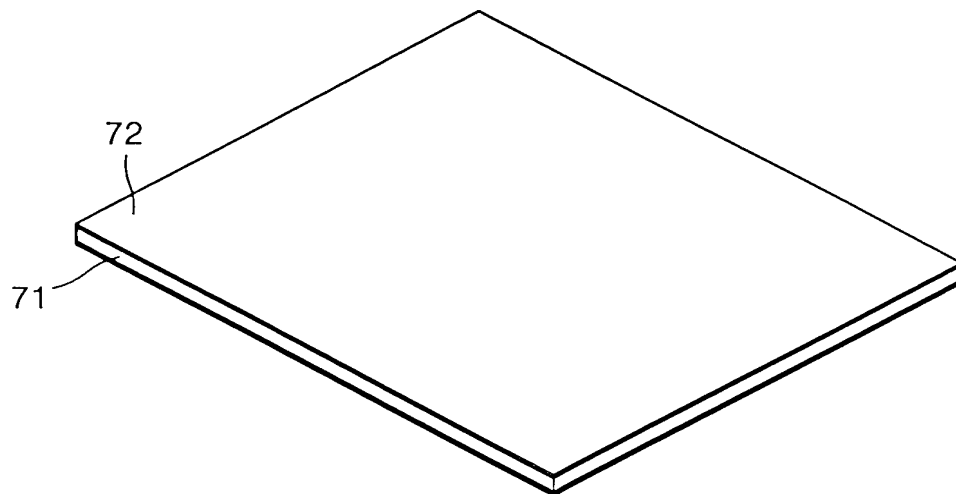


FIG. 11

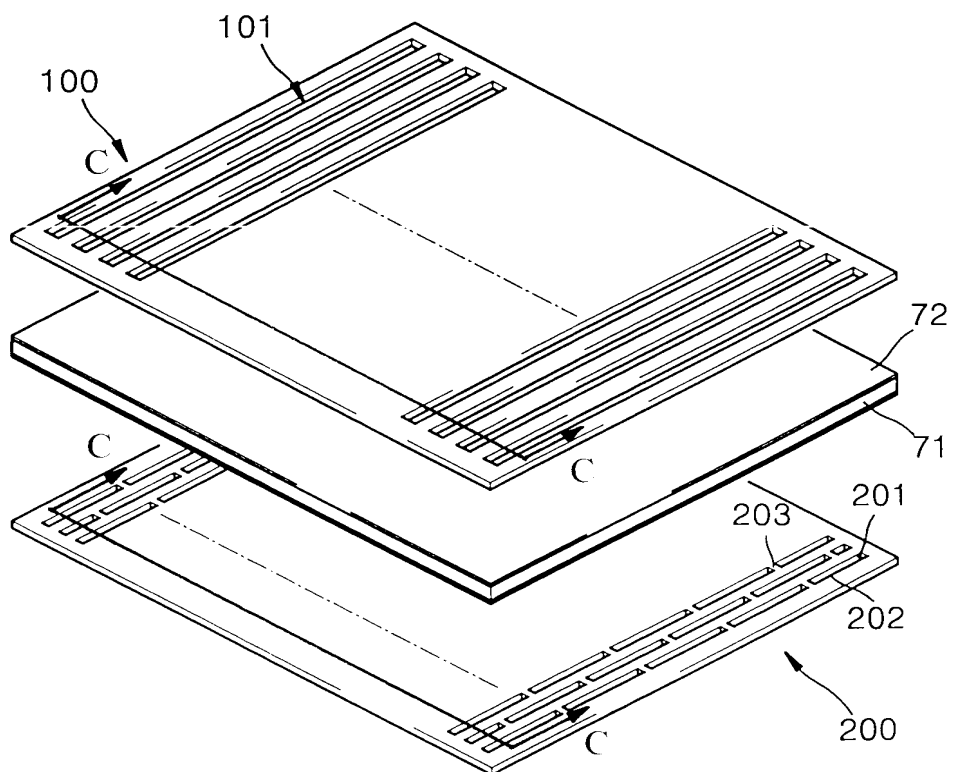


FIG. 12

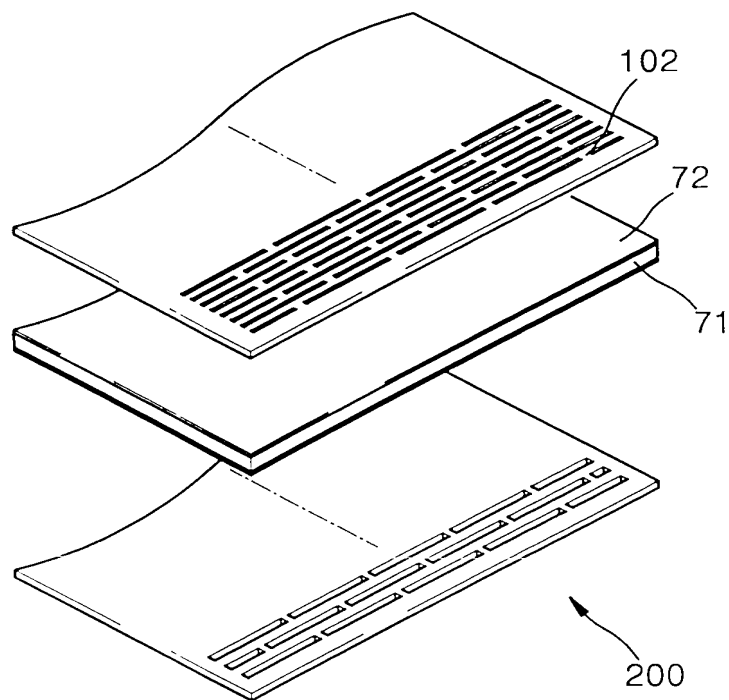


FIG. 13

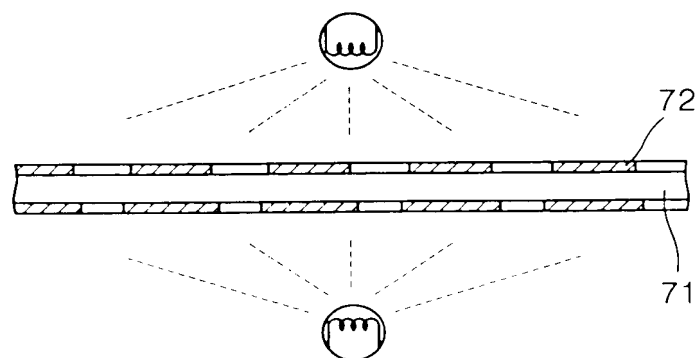




FIG. 14

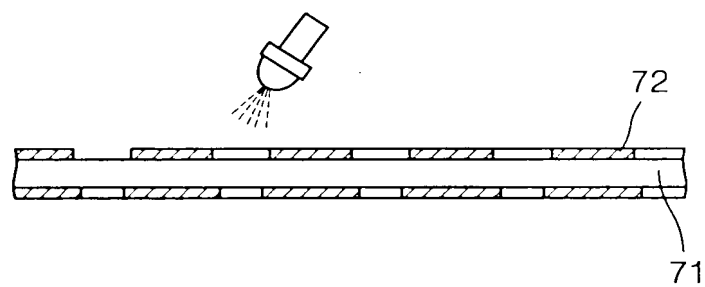


FIG. 15

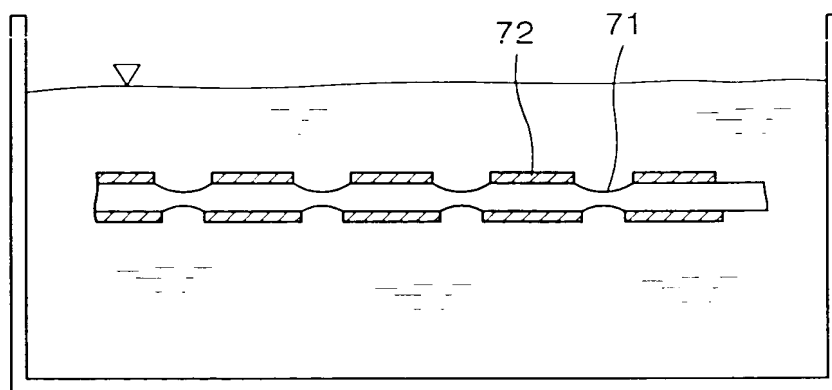


FIG. 16

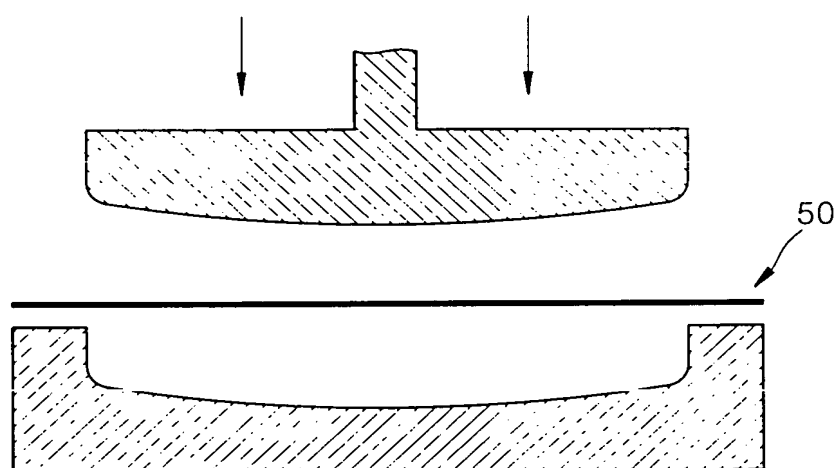


FIG. 17 is a schematic diagram of a device 45.

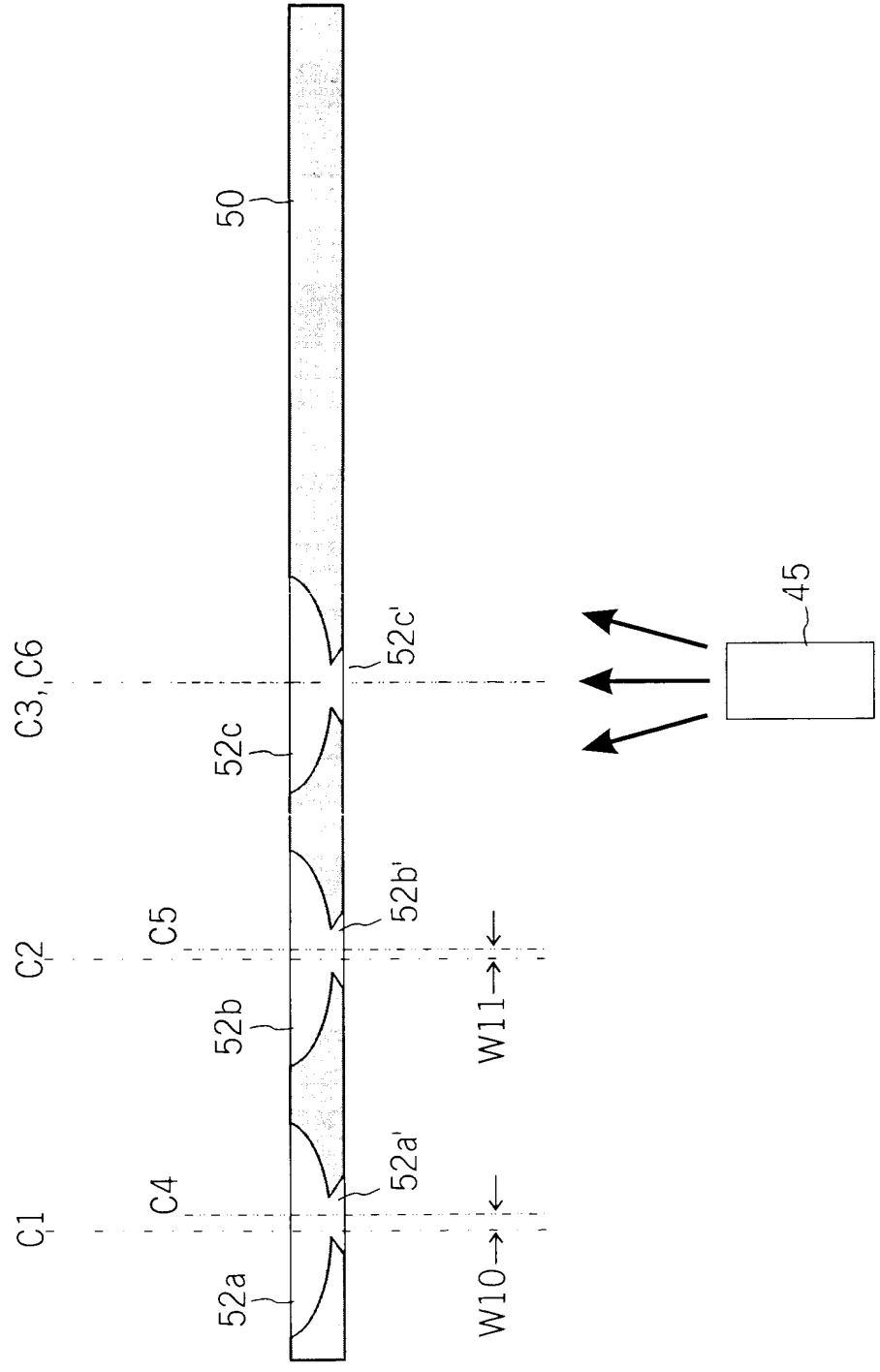
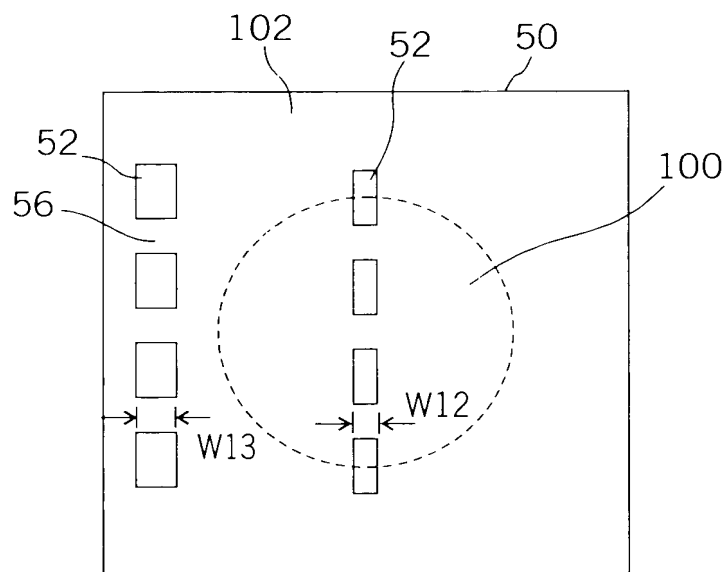
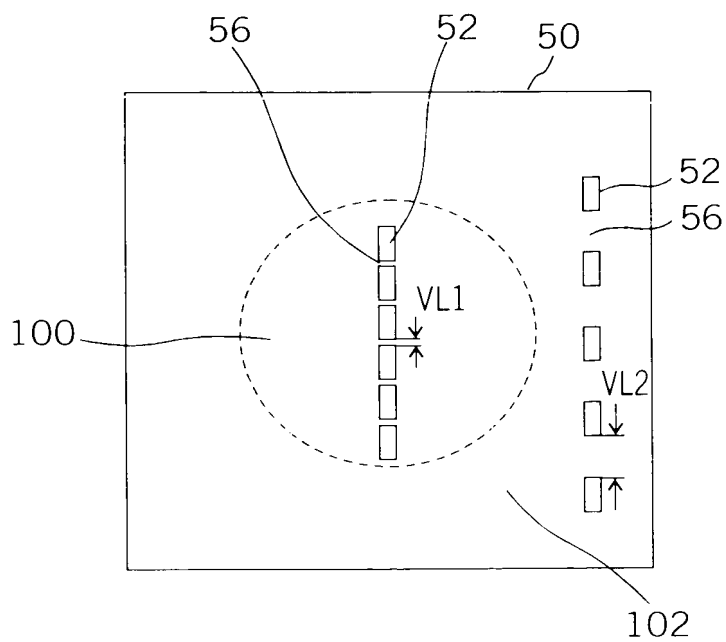


FIG. 17



**FIG. 18**



**FIG. 20**

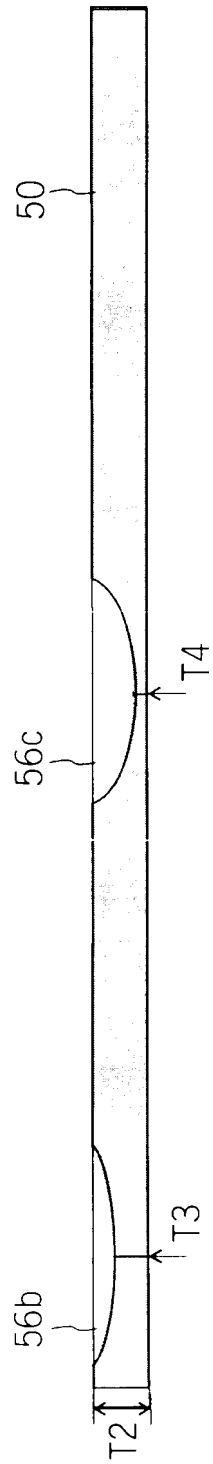


FIG. 19